

AMENDMENTS TO THE SPECIFICATION

Please amend the following paragraph as indicated.

[0034] Switch 410 may be implemented via a standard microcomputer (CPU), a Digital signal Processor (DSP), a Field Programmable Gate Area (FPGA), an Application Specific Integrated Circuit (ASIC), or some combination of these techniques. In essence it acts as an ad-hoc router, knowing how to route packets one way or another depending on which way is currently best. In this implementation, switch 410, and more generally NIU 405, act as a node in an ad-hoc wireless network collectively formed by all the NIUs in an area. If the wireline connection is functioning properly, switch [[407]] 410 will connect its subscriber via the wireline connection.